

REMARKS

The Final Office Action mailed April 19, 2005 has been received and carefully noted.

The following remarks are submitted as a full and complete response thereto.

Claims 1-7 and 9-17 are pending in the present application. A "Petition for Extension of Time" for extending the due date for responding to the Office Action by one month and a credit card payment form to cover the fee payment (\$120.00) for the extension are filed with this Response. Authorization is granted to charge counsel's Deposit Account No. 01-2300, referencing Attorney Docket No. 024016-00008, for any additional fees necessary for entry of this Response.

Dependent claim 17 stands objected to as being of improper dependent form. Specifically, the Examiner states that claim 17 does not add any limitation to claim 1. The objection is respectfully traversed and reconsideration is requested.

Independent claim 1 is directed to, in pertinent part, that a plurality of detecting units for detecting predetermined marks detect synchronization from a plurality of parallel data. Dependent claim 17 further defines the plurality of detecting units also detect the corresponding each of the plurality of parallel data. Support for this claim can be found in the Specification at p. 52, ls. 5-21. Reconsideration is accordingly respectfully requested.

Claims 1-7 and 9-17 stand rejected under 35 U.S.C. § 102(e) as being anticipated by the Wilson et al. patent (U.S. Patent No. 6,118,603). Dependent claims 2-7, 9 and 17 depend from independent claim 1. Dependent claims 12-16 depend from independent claim 11. The rejections are respectfully traversed and reconsideration is requested.

Independent claim 1 recites a data processor comprising a receiving unit for receiving a series of data including a predetermined mark for detecting synchronization and generating a plurality of parallel data from the series of data; and a plurality of detecting units for detecting the predetermined mark for detecting synchronization from the plurality of parallel data.

Independent claim 10 recites a data processor for detecting the predetermined marks for detecting synchronization included in a series of data read from a memory medium in order to establish synchronization at a time of transferring the series of data to a controller unit from a read channel unit, comprising similar features. Independent claim 11 recites a data processing method of the same. It is respectfully submitted that the Wilson et al. patent does not disclose or suggest each and every element of the data processor or method of the present invention.

The present invention includes a plurality of detecting units for detecting a predetermined mark for detecting synchronization from parallel data. With reference to Fig. 2, the parallel data of 4 bits is input to four synchronization detectors. With reference to Fig. 3, relating to a flip-flop FF(0) to a flip-flop FF(n) of four bits in a shift registered, even where the order of data positions of predetermined marks for detecting synchronization is any one of the types o-line, Δ -line, \square -line and \times -line, any one of the four detectors can detect the predetermined mark for detecting synchronization. The predetermined marks for detecting synchronization can be detected from any bit position of the parallel data.

The Wilson et al. patent discloses a method and system of encoding and decoding servo information on a removable disk cartridge. Specifically, with reference to Fig. 5, the read signal 502 is a peak-detected, digitized signal which originates from the disk drive recording channel. In response to read data 502, data sector 504 produces a synchronized serial servo data signal. The serial servo data signal 508 is provided to a special shift register 510 which is a multiple of

12 bits in length to accommodate a 12-bit servo synchronization mark. The shift register 510 prepares a parallel data signal 512, which is applied to two mark detectors 515 and 516. These mark detectors detect, Mark_0 and Mark_1 characters. (col. 7, ln. 39 – col. 8, ln 18)

According to the present invention, which detects the parallel data of the predetermined mark for detecting synchronization with the plurality of detecting units, the predetermined mark for detecting synchronization can be detected from any bit position of the parallel data. The present invention is specifically provided with the plurality of detecting units for detecting the predetermined mark for detecting synchronization in a predetermined bit width among the series of data in the parallel condition. Thus, any one of the detecting units can detect the predetermined mark for detecting synchronization to confirm the synchronous data.

In contrast, the Wilson et al. patent includes the two mark detectors 515 and 516 which are separate detectors for detecting different marks, or Mark_0 and Mark_1 characters, respectively. Those detectors merely detect marks by parallelizing serial data. If the mark detectors 515 and 516 fail to detect respective synchronization marks, the synchronization could not be detected. Accordingly, the Wilson et al. patent fails to disclose or suggest the data processor and data processing method as claimed in the present invention.

Based upon the forgoing, Applicant respectfully submits that each and every element recited within independent claims 1 and 10-11 is neither disclosed nor suggested by the Wilson et al. patent, and therefore is patentable and in condition for allowance. Reconsideration is requested.

It is further submitted that dependent claims 2-7, 9 and 17 and dependent claims 12-16 are also patentable and in condition for allowance due to their dependency upon independent claims 1 and 11, respectively, since the dependent claims differ in scope from the corresponding


parent claims. Dependent claims 2-7, 9 and 17 depend from independent claim 1 and dependent claims 12-16 depend from independent claim 11, and thus are further limited to additional features of the invention. Therefore, it is respectfully submitted that the dependent claims are patentable over the Wilson et al. patent for at least the reasons set forth above with respect to independent claims 1 and 11. Reconsideration is requested.

Entry of this Response after final rejection is therefore submitted as proper in that it places the application in condition for allowance. Particularly, the present Response is submitted as not raising new issues or requiring further consideration or searching. Undersigned counsel would accordingly appreciate the Examiner telephoning counsel prior to the expiration of the six-month statutory period (i.e., October 19, 2005) to indicate the disposition of this Response.

If for any reason the Examiner determines that the application is not now in condition for allowance, it is respectfully requested that the Examiner contact the Applicant's undersigned counsel at the telephone number, indicated below, to arrange for an interview to expedite the disposition of this application.

Dated: August 19, 2005

Respectfully submitted,


Marylee Jenkins
Registration No. 37,645
Attorney for Applicant

Customer No. 004372
ARENT FOX, PLLC
1675 Broadway
New York, New York 10019
Tel: (212) 484-3928
Fax: (212) 484-3990

MLJ/my